



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Ernst Muellner et al.

Appl. No.: 09/941,082

Conf. No.: 5767

Filed: August 28, 2001

Title: CLOCK AND DATA REGENERATION WITH DEMULTIPLEXER  
FUNCTION

Art Unit: Unknown

Examiner: Unknown

Docket No.: 112740-298

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P.O. Box 1450  
Alexandria, VA 22313-1450

## CERTIFICATE OF MAILING BY FIRST CLASS MAIL UNDER 37 CFR 1.8

Sir:

I hereby certify that the following documents relating to the above-identified application:

1. Transmittal of Information Disclosure Statement in duplicate;
2. IDS, PTO 1449, references; and
3. return receipt postcard.

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on June 9, 2003.

Respectfully submitted,

BELL, BOYD & LLOYD LLC

Robert Buccieri

Name of Person Mailing Correspondence

Signature

2831  
#6  
SP  
07-02-03

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.  
112740-298

In Re Application Of: **Ernst Muellner et al.**

JUN 12 2003

Serial No.  
09/941,082

Filing Date  
August 28, 2001

Examiner

Group Art Unit

Title: **CLOCK AND DATA REGENERATION WITH DEMULTIPLEXER FUNCTION**



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Address to:  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Technology Center 2600

**37 CFR 1.97(b)**

1.  The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

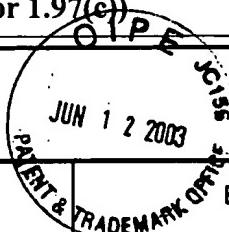
**37 CFR 1.97(c)**

2.  The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:
  - the statement specified in 37 CFR 1.97(e);  
**OR**  
 the fee set forth in 37 CFR 1.17(p).

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.  
112740-298

In Re Application: **Ernst Muellner et al.**



Serial No.  
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**CLOCK AND DATA REGENERATION WITH DEMULTIPLEXER FUNCTION**

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**Payment of Fee**

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- A check in the amount of \_\_\_\_\_ is attached.
- The Assistant Commissioner is hereby authorized to charge and credit Deposit Account No. 02-1818 as described below. A duplicate copy of this sheet is enclosed.
- Charge the amount of \_\_\_\_\_
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- Charge any additional fee required.

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\*This certificate may only be used if paying by deposit account.

*WE V*  
*Signature*

Dated: June 9, 2003

William E. Vaughan (Reg. No. 39,056)

Bell, Boyd & Lloyd LLC

P.O. Box 1135

Chicago, Illinois 60690-1135

CC:



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### INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 37 C.F.R. 1.97, and 37 C.F.R. 1.98, Applicants request that a citation and examination of the references cited below, and on the attached PTO-1449 form, copies of which are enclosed, be made during the course of examination of the above-identified application for United States patent.

#### FOREIGN PATENT DOCUMENTS

<u>Document No.</u>	<u>Date</u>	<u>Country</u>
DE 197 17 586 C1		Germany

#### OTHER DOCUMENTS

IEEE Journal of Solid State Circuits, Vol. 34, No. 9, September 1999, pages 1320-1324.

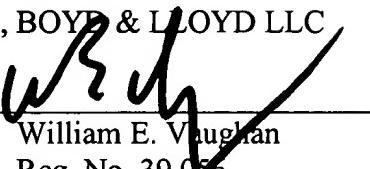
German DE 197 17 586 C1 relates to a clock and data recovery circuit for high data rate which has first and second flip-flop stages which provide parallel data streams with half data rate of input data signal, two EXOR gates controlled by a third D-Flip-flop stage and a subtractor whose output controls an oscillator.

Applicants look forward to early and favorable consideration of this matter.

Respectfully submitted,

BELL, BOYD & LOYD LLC

BY

  
William E. Vaughan

Reg. No. 39,056

P.O. Box 1135

Chicago, Illinois 60690-1135

Phone: (312) 807-4292

Dated: June 9, 2003



<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b> (Use several sheets if necessary)	Atty Docket No. <b>112740-298</b>	Application No. <b>09/941,082</b>
	<b>Applicant</b> <b>Muellner et al.</b>	
PTO Form 1449	<b>Filing Date</b> <b>8-28-01</b>	<b>Group</b> <b>      </b>

## **U.S. PATENT DOCUMENTS**

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## **FOREIGN PATENT DOCUMENTS**

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

Examiner's Initials	OTHER DOCUMENTS ( <i>Including Author, Title, Date, Pertinent Pages, Etc.</i> )
	IEEE Journal of Solid State Circuits, Vol. 34, No. 9, September 1999, pages 1320-1324

**Examiner:** \_\_\_\_\_ **Date Considered:** \_\_\_\_\_

\*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.